

R.sugashini, M.E (Ph.D)
Teaching Fellow (EEE)
Dept of Electrical and Electronics Engineering
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OBJECTIVE

To work in a stimulating and challenging environment that would facilitate the maximum utilization and application of my broad skills and expertise in making a positive difference to the organization.

ACADEMIC QUALIFICATION

EDUCATION	INSTITUTION	BOARD/MAJOR	YEAR OF PASSING	SCORE/CGPA
Ph.D (Pursing)	Anna University, Chennai	Electrical Engineering	2023(Expected)	Completed Confirmation(Status)
M.E	Rajalakshmi Engineering College, Thandalam.	Power Electronics and Drives	2015	8.55 (First Class With Distinction)
B.E	Anand Institute of Higher Technology, Kazhipattur.	Electrical and Electronics Engineering	2013	7.44 (First Class)
Grade 12	Jawahar Higher Secondary school, Neyveli.	State board	2009	84.9%
Grade 10	Jawahar Higher Secondary school(CBSE), Neyveli.	CBSE	2007	74%

AREAS OF INTEREST

- ✓ Power Electronics.
- ✓ Electrical Machines.
- ✓ Microprocessor and Microcontroller

TECHNICAL SKILLS

- ❖ Programming Languages known : Basics of C, C++.
- ❖ Software's known : MS office, and MATLAB/simulink.

UG PROJECT

TITLE : Design and implementation of flyback Inverter.

DESCRIPTION : The main aim of this project is to formulate and implement a design methodology of flyback inverter for AC photovoltaic modules and to focus on choosing the inverter design parameters and considering PV module characteristics and topology constraints.

PG PROJECT

TITLE : Isolated High Step Up DC-DC Boost to Boost Z-Source converter.

DESCRIPTION : The main aim of this project is to increase the voltage by connecting two Z-Source converter in series connected to single phase inverter in order to run a single phase induction motor.

INPLANT TRAINING VISITS

- ✓ Undergone In-Plant training at Port trust in Chennai.
- ✓ Undergone In-plant training at BSNL in Trichy.

WORK EXPERIENCE

Working as Teaching Fellow in the Department of Electrical and Electronics Engineering at University college of Engineering, Panruti campus, Anna university from 18.02.2016 to till.

SUBJECTS HANDLED

- High Voltage Engineering(2Times)
- Microprocessor and Microcontroller (3Times)
- Solid State Drives
- Transmission and Distribution
- Basic Electrical Electronics and Measurement Engineering (2Times)
- Electronic Devices
- Digital Logic Circuits(2Times)
- Electrical Machines (2Times)
- Linear Integrated Circuits and Application(2Times)
- Basic Electrical Electronics Engineering (2Times)

WORKSHOP/CONFERENCES

- ✓ Attended an **International Conference** on “**Power Electronics and Renewable Energy Systems**” (**ICPRES**)’14 conducted by REC, Chennai, on April 25th and 26th 2014.
- ✓ Attended an “**International Conference on Recent Trends in Science, Engineering and Technology**” (**ICRSET**)’19 conducted by CK College of Engineering and Technology, Cuddalore on March 9th 2019.
- ✓ Attended **Regional Conference** on “Innovations in Engineering & Research” during 14th and 15th November 2014 at REC, Chennai.
- ✓ Attended “**One day national workshop on Recent Developments in Research Opportunities’ in Smart Grid**” conducted by REC, Chennai.
- ✓ Attended **UGC Sponsored workshop on Embedded Control For PV Fed Electric Drive Systems (ECPV FED EDS’15)** during (9/3/15 to 4/3/15) conducted by Annamalai university, Chidambaram.
- ✓ Attended TEQIP III Sponsored Five days online Workshop on “**Advancements in Electrical Engineering: An Academic & Industrial Approach**” conducted by National Institute of Technology, Silchar from 3rd to 7th August, 2020
- ✓ Attended TEQIP-III Sponsored International Workshop on “**Electric Power Grid Modernisation: Trends, Challenges and Opportunities**” held from July 20-24, 2020 conducted by The National Institute of Engineering, Mysuru.

FACULTY DEVELOPMENT PROGRAM

- ✓ Participated in the One Week Faculty Development Program on “**Research Challenges in Power Systems (R-CHIPS-2020)**” from 13th to 18th July, 2020.
- ✓ Participated in Five day virtual International Faculty Development Program on “**Research Orientations in Engineering with Effective Outcomes**” held from 26th to 30th July, 2020.

- ✓ Participated in online One Week Faculty Development Program on “Recent Trends in Electrical Energy Conservation and Grid Technology” Conducted by Velammal College of Engineering and Technology, Madurai from 12.10.2020 to 19.10.2020

INTERNATIONAL JOURNAL

- ✓ Published paper in “**International Journal of Applied Engineering Research**” entitled “**Isolated High Step up DC-DC boost to boost Z Source Converter**” ISSN 0973-4562 Volume 10, Number 6 (2015).
- ✓ Published Paper in “**International Conference on Recent Trends in Science, Engineering and Technology**” entitled **Compensation of Active Power and Non Active Power by Using SVC and Capacitive Grid Connected Inverter**” ISSN NO.1076-5131 Volume VI Issue 3, March2019.

EXTRA-CURRICULAR ACTIVITIES

- ✓ District Level Hockey Player.

PERSONAL DETAILS

Date of Birth	:	04.08.1991
Father's name	:	M.Rajarathinam
Mother's name	:	C.Palaniyammal
Languages Known	:	English and Tamil
Nationality	:	Indian
Address	:	No,431 East street, Sorathur and post, panruti taluk Cuddalore district, PIN-607805.

DECLARATION

I hereby declare that all the information mentioned above are true to the best of my knowledge and belief.

PLACE :

DATE :

(R.Sugashini)